ScicosLab Pack for Scicoslab v4.4.1
Code generator for FLEX and EASYLAB
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- Getting Started

1. Download and install Cygwin from the Cygwin site
   Cygwin is required to compile the code generated from your Scicos diagram.

2. Download and install Microsoft Visual C++ 2008 from the Microsoft Visual C++ site
   It is required from the Evidence Scicoslab pack.

3. Download and install ScicosLab 4.4.1 from the ScicosLab site

4. Download and install Microchip MPLAB IDE from the MPLAB IDE site
   It is required to program the dsPIC microcontroller mounted on the FLEX board.
   Then download and install a C30 compiler, for example from the
   Microchip MPLAB C30 compiler site. A compiler is required to compile your projects.
   To program the dsPIC microcontroller you need a programmer.
   You can buy a programmer for dsPIC from Microchip site.
   Programmers suggested are: Microchip MPLAB ICD2 or Microchip MPLAB ICD3.
   See the Microchip site for more informations.

5. Download the latest Scicoslab pack from the ScicosLab pack download page.
   Unzip the pack and install it with the following procedure.

- Installation

   To install and uninstall the pack execute the 'installer.sce' script into ScicosLab (File -> Exec...).
   If needed, run ScicosLab with ADMINISTRATOR privileges.
   It is required if the Scicoslab main directory is in a protected path.
   At the end of the installation restart ScicosLab for the changes to take effect.
   Create and compile your first Scicos diagram as shown here.
   Then flash the microcontroller, test your application and ... have fun!

Please note, this version of the ScicosLab pack includes a stand-alone version of RT-Druid
and a full version of Erika Enterprise to simplify the installation procedure.

If you have any problem please contact us using the Erika Forum.